IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	TIEN-JEN CHANG, ET AL.) Group Art Unit: 1753
Serial No.:	10/707,047	
Filed:	November 18, 2003) Examiner: Van, L.
For:	METHOD FOR SELECTIVE ELECTRO- PLATING OF SEMICONDUCTOR DEVICE I/O PADS USING A)) Confirmation No.: 1046)

AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In response to the Office Action mailed May 4, 2006, please amend the Application as follows: